

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Yukio YAMAUCHI et al.

Serial No. 09/266,012

Filed: March 11, 1999

For: Thin Film Transistor,

Electroluminescence Display Device

And Manufacturing Method Of The

Same

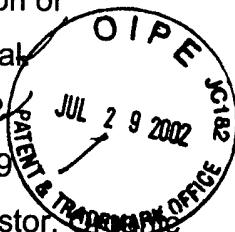
Art Unit: 2822

Examiner: N. D. Richards

RECEIVED

AUG 6 2002

TECHNOLOGY CENTER 2800



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on 7/22/02

#23/E
J.M.G.
8/8/02

AMENDMENT AND REQUEST FOR INTERFERNCE UNDER 37 CFR 1.607

Honorable Commissioner of Patents
Washington, D.C. 20231
Sir:

In response to the Office Action dated March 20, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

- E 1
1. An electroluminescence display device comprising:
a substrate having an insulating surface;
a thin film transistor formed over said substrate, said thin film transistor comprising an active layer comprising crystalline silicon including source, drain and channel regions;
an electrode comprising aluminum electrically connected to one of said source and drain regions;
a barrier metal layer interposed between said electrode and said one of the source and drain regions to prevent a direct contact therebetween;
a transparent electrode electrically connected to said thin film transistor; and
an electroluminescence layer comprising an organic material adjacent to said transparent electrode,
wherein said barrier metal layer comprises titanium.